

WHAT IS CLAIMED IS:

- 1 1. An apparatus, comprising:
2 a detector circuit to detect a processor type; and
3 a voltage provider circuit to provide a low-power state processor voltage in
4 accordance with the processor type.
- 1 2. The apparatus of claim 1, wherein the detector circuit is to detect the processor
2 type in accordance with a signal from a processor pin.
- 1 3. The apparatus of claim 2, wherein the detector circuit includes:
2 a transistor having a base to receive the signal from the processor pin, wherein the
3 transistor is on when a first processor type is present and off when a second processor
4 type is present.
- 1 4. The apparatus of claim 3, wherein the transistor comprises a bi-polar junction
2 transistor and the collector of the transistor provides a processor type signal to the voltage
3 provider circuit.
- 1 5. The apparatus of claim 1, wherein the voltage provider circuit provides a first
2 voltage level when a first processor type is detected and a second voltage level when a
3 second processor type is detected.

1 6. The apparatus of claim 5, wherein the voltage provider circuit includes:
2 a voltage divider.

1 7. The apparatus of claim 6, wherein the voltage provider circuit receives a
2 processor type signal from the detector circuit and further includes:
3 a transistor to adjust a resistance associated with the voltage divider in accordance
4 with the processor type signal.

1 8. The apparatus of claim 7, wherein the transistor comprises an n-channel
2 inverter.

1 9. The apparatus of claim 1, further comprising:
2 an offset voltage circuit to adjust an offset value associated with a processor
3 voltage in accordance with the processor type.

1 10. The apparatus of claim 1, wherein the detector circuit and the voltage
2 provider circuit are associated with a voltage regulator integrated circuit.

1 11. An apparatus, comprising:
2 a detector circuit to detect a processor type; and
3 a voltage provider circuit to provide a processor voltage in accordance with the
4 processor type.

1 12. The apparatus of claim 11, wherein a first voltage level is provided when a
2 first processor type is detected and a second voltage level is provided when a second
3 processor type is detected.

1 13. An apparatus, comprising:
2 an input to receive a signal associated with a processor type; and
3 an output to provide a low-power state processor voltage in accordance with the
4 processor type.

1 14. The apparatus of claim 13, wherein a first voltage level is provided for a first
2 processor type and a second voltage level is provided for a second processor type.

1 15. The apparatus of claim 13, wherein the input receives an input signal from a
2 processor pin.

1 16. The apparatus of claim 13, wherein the output provides an output signal to a
2 processor pin.

1 17. An apparatus, comprising:
2 a first transistor, including
3 a base to receive a signal from a processor pin, the first transistor being on
4 when a first processor type is present and off when a second processor type is
5 present, and
6 a collector to provide a processor type signal; and
7 a second transistor, including:

8 a gate to receive the processor type signal,
9 wherein the second transistor is to adjust a resistance associated with a voltage
10 divider such that one of a first and a second low-power state processor voltage is
11 provided from the voltage divider in accordance with the processor type signal.

1 18. The apparatus of claim 17, wherein the apparatus comprises a voltage
2 regulator integrated circuit.

1 19. A method, comprising:
2 detecting a processor type; and
3 providing a low-power state processor voltage in accordance with the processor
4 type.

1 20. The method of claim 19, wherein a first voltage level is provided when a first
2 processor type is detected and a second voltage level is provided when a second
3 processor type is detected.

1 21. The method of claim 19, further comprising:
2 adjusting an offset value in accordance with the processor type.

1 22. A system, comprising:
2 a power supply to convert alternating current power to direct current power; and
3 a voltage regulator coupled to the power supply and including:
4 a detector circuit to detect a processor type, and

5 a voltage provider circuit to provide a low-power state processor voltage
6 in accordance with the processor type.

1 23. The system of claim 22, wherein a first voltage level is provided when a first
2 processor type is detected and a second voltage level is provided when a second
3 processor type is detected.